FIG.1

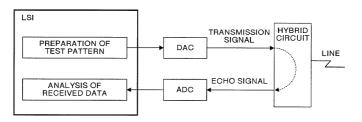
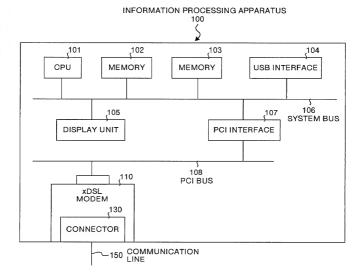


FIG.2



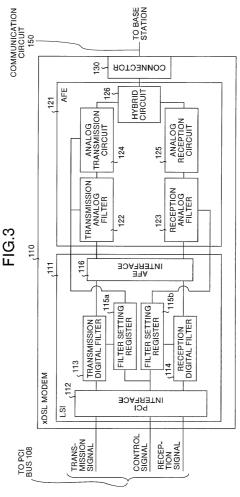


FIG.4

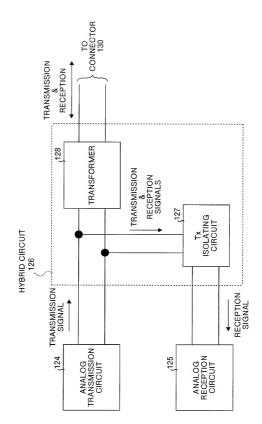
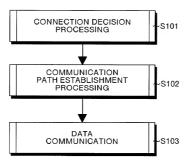


FIG.5



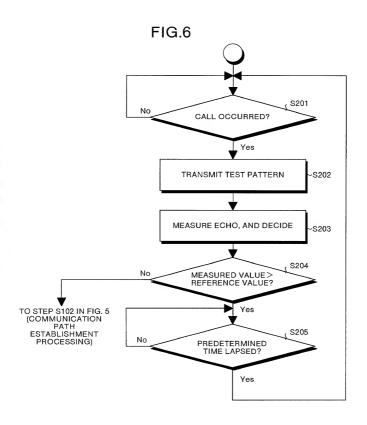


FIG.7

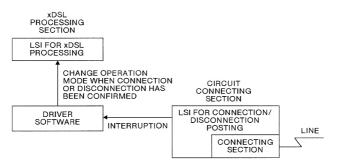


FIG.8

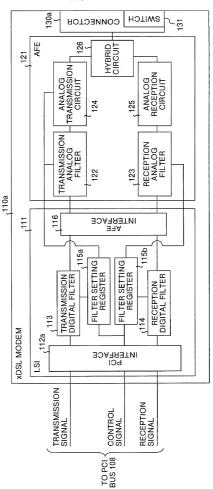


FIG.9

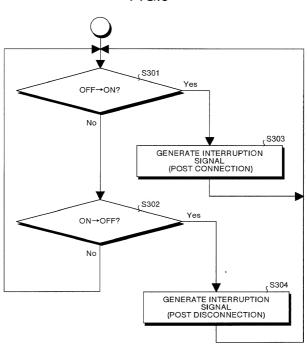


FIG.10

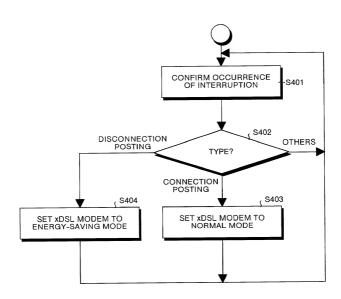


FIG.11

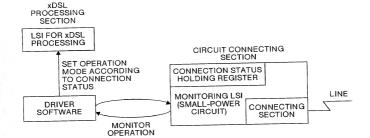


FIG.12

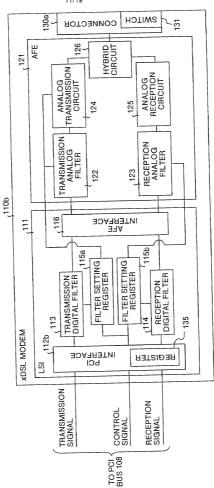


FIG.13

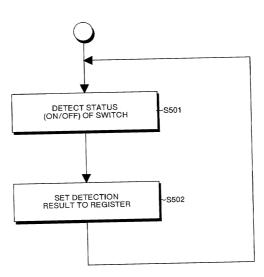


FIG.14

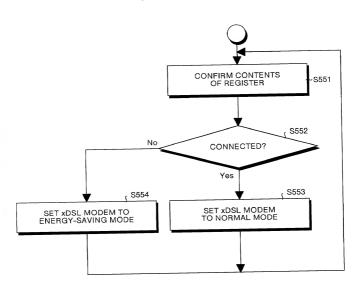


FIG.15

| | | STATUS OF POWER SOURCE | |
|-------------------|--|------------------------|-------------------------------|
| | | EXTERNAL POWER MODE | INTERNAL POWER SOURCE MODE |
| ERROR CONTENTS | ERROR IN SOFTWARE PROCESSING | L TIMES | M TIMES |
| | NO RESPONSE FROM THE OTHER PARTY | L TIMES | N TIMES |

WHERE N<M<L

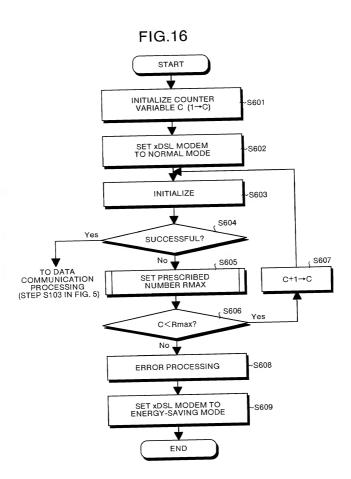


FIG.17

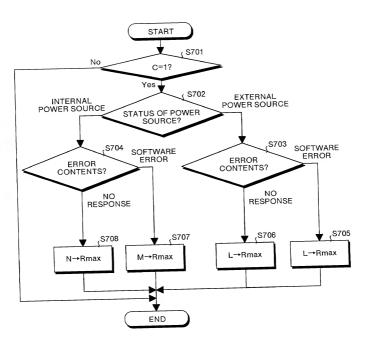


FIG.18

| | | STATUS OF POWER SOURCE | |
|-------------------|---------------------------------------|------------------------|-------------------------------|
| | | EXTERNAL POWER MODE | INTERNAL POWER SOURCE MODE |
| ERROR CONTENTS | ERROR IN SOFTWARE PROCESSING | t1 | t2 |
| | NO RESPONSE FROM THE OTHERPARTY | t2 | t2 |

WHERE t1<t2

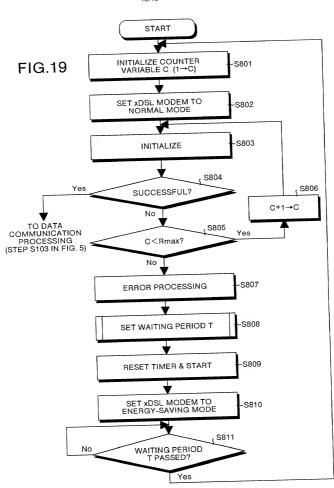


FIG.20

